

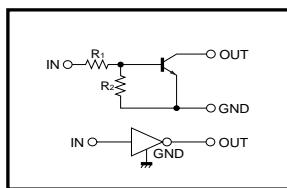
Digital transistor (built-in resistors)

DTC144VUA / DTC144VKA / DTC144VSA

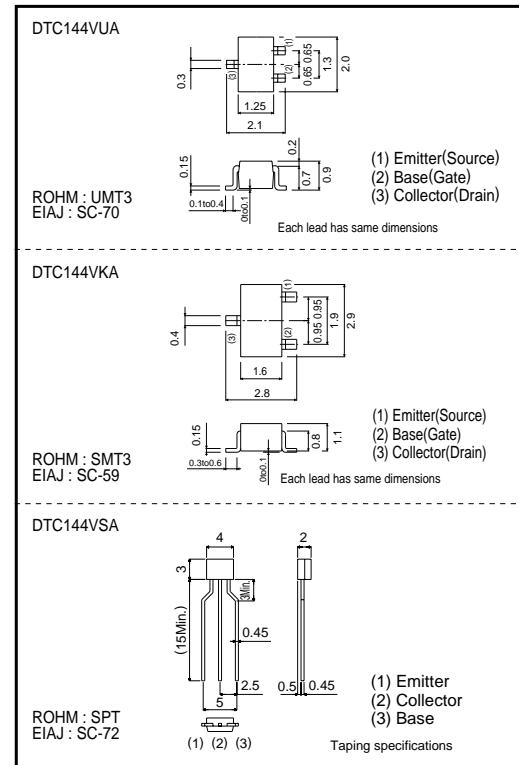
●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on / off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

●Equivalent circuit



●External dimensions (Unit : mm)



●Absolute maximum ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Supply voltage	V_{cc}	50	V
Input voltage	V_i	-10 to +40	V
Output current	I_o	30	mA
	$I_c(\text{Max.})$	100	
Power dissipation	P_d	200	mW
DTC144VUA / DTC144VKA		300	
DTC144VSA			
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

●Packaging, marking and packaging specifications

Type	DTC144VUA	DTC144VKA	DTC144VSA
Package	UMT3	SMT3	SPT
Marking	166	E66	-
Packaging code	T106	T146	TP
Basic ordering unit (pieces)	3000	3000	5000

●Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{i(\text{off})}$	-	-	1	V	$V_{cc}=5\text{V}$, $I_c=100\mu\text{A}$
	$V_{i(\text{on})}$	6	-	-		$V_o=0.3\text{V}$, $I_o=2\text{mA}$
Output voltage	$V_{o(\text{on})}$	-	0.1	0.3	V	$I_o=10\text{mA}$, $I_i=0.5\text{mA}$
Input current	I_i	-	-	0.16	mA	$V_i=5\text{V}$
Output current	$I_{o(\text{off})}$	-	-	0.5	μA	$V_{cc}=50\text{V}$, $V_i=0\text{V}$
DC current gain	G_i	33	-	-	-	$I_o=5\text{mA}$, $V_o=5\text{V}$
Input resistance	R_1	32.9	47	61.1	kΩ	-
Resistance ratio	R_2/R_1	0.17	0.21	0.26	-	-
Transition frequency	f_T	-	250	-	MHz	$V_{ce}=10\text{V}$, $I_c=-5\text{mA}$, $f=100\text{MHz}$

* Transition frequency of the device.